



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kusumoto et al.                      Art Unit : 2814  
Serial No. : 09/903,339                      Examiner : Theresa T. Doan  
Filed : July 10, 2001  
Title : METHOD FOR PRODUCING INSULATED GATE THIN FILM  
SEMICONDUCTOR DEVICE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	6	-	42	=	0	\$0
Independent	2	-	12	=	0	\$0
First Presentation of Multiple Dependent Claims						\$0

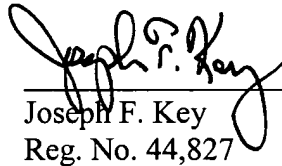
Applicant hereby petitions under 37 C.F.R. §1.136 for a 2 month extension of time. \$450

TOTAL FEE DUE \$450

A check for \$450 is attached.

Respectfully submitted,

Date: 3/22/2005

  
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